

FIG. 1 is a block diagram of a system for processing a video signal. The system includes a tuner (12), an ATSC decoder (16), a PSIP enhancer (18), a DTV modulator (20), and a transmitter (22). The tuner (12) receives a video signal (10) and outputs a signal to the ATSC decoder (16). The ATSC decoder (16) outputs a signal to the PSIP enhancer (18). The PSIP enhancer (18) outputs a signal to the DTV modulator (20). The DTV modulator (20) outputs a signal to the transmitter (22). The transmitter (22) outputs a signal (30). A PC (32) is connected to the PSIP enhancer (18) via a bus (24, 28, 34). The bus includes a SET CHANNEL input to the tuner (12) and a SET CHANNEL output from the transmitter (22).

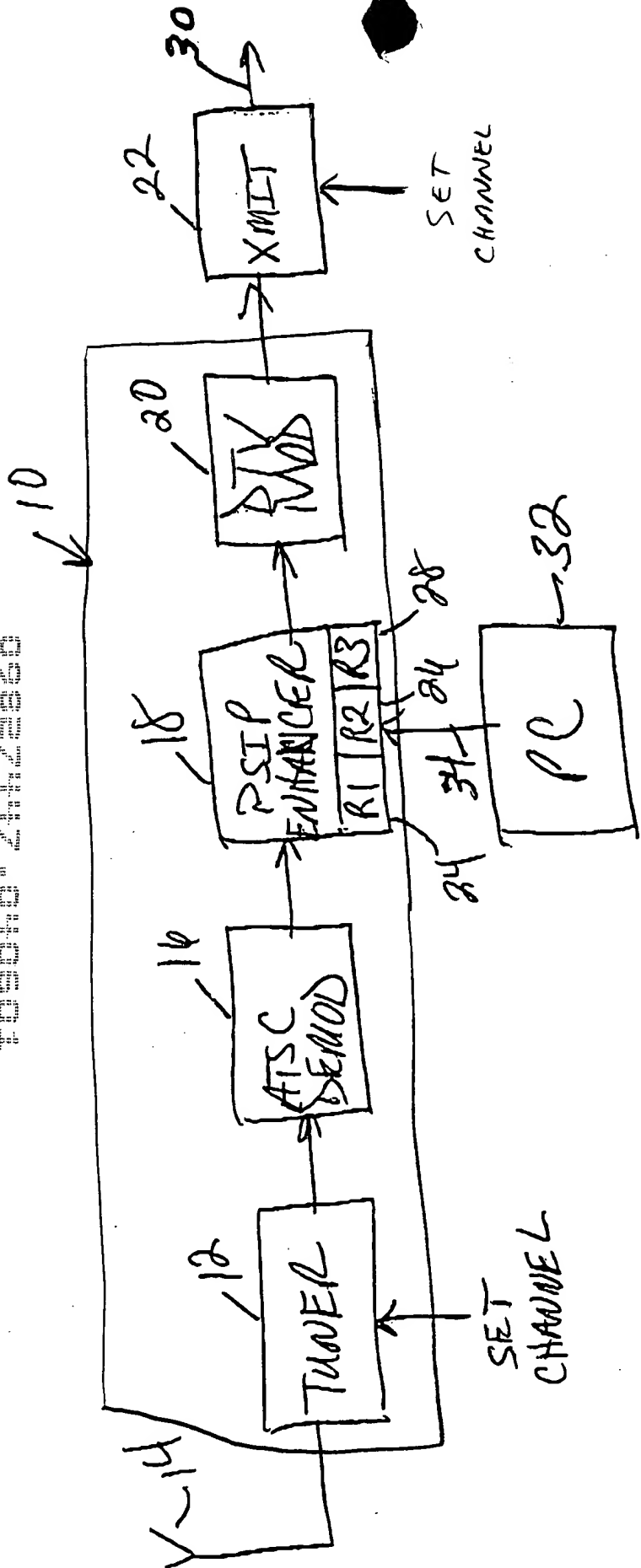


FIGURE 1

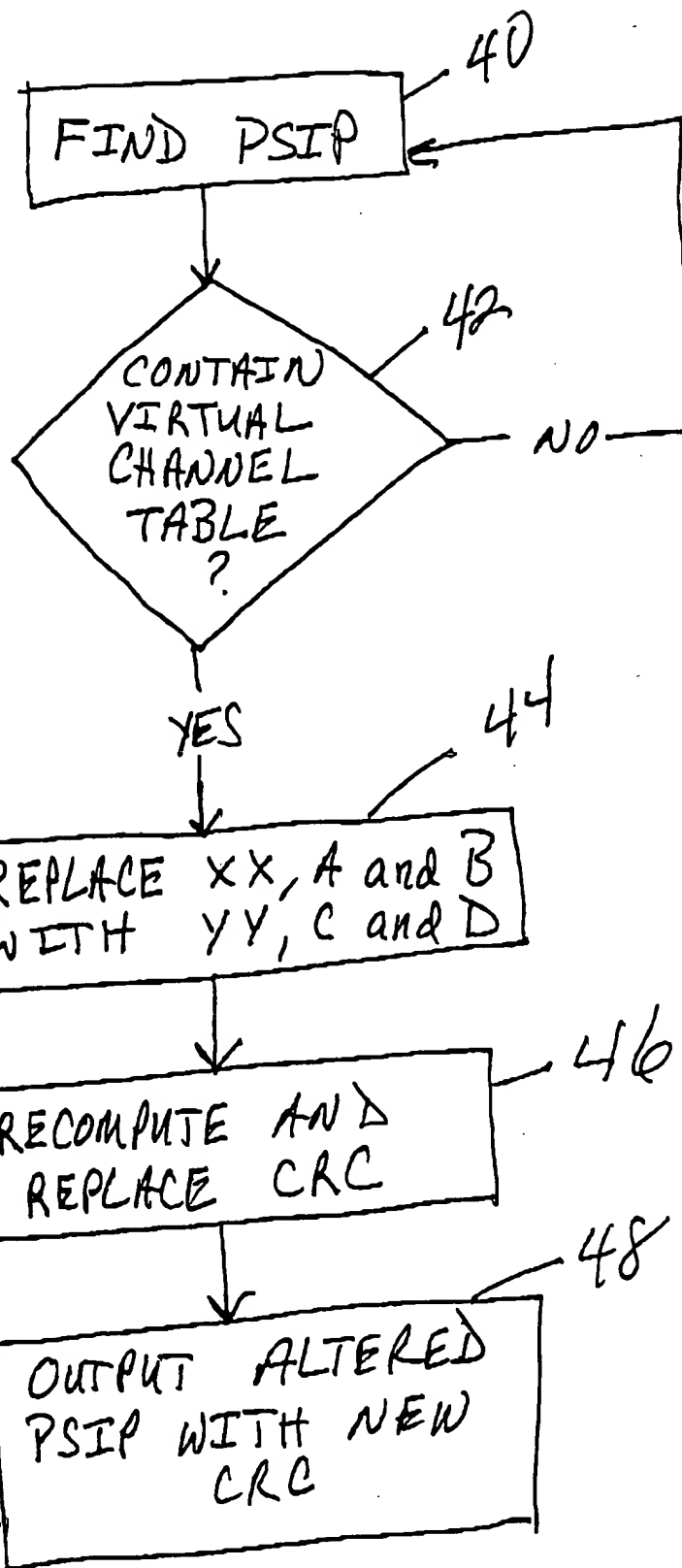


FIGURE
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